

FIG. 1

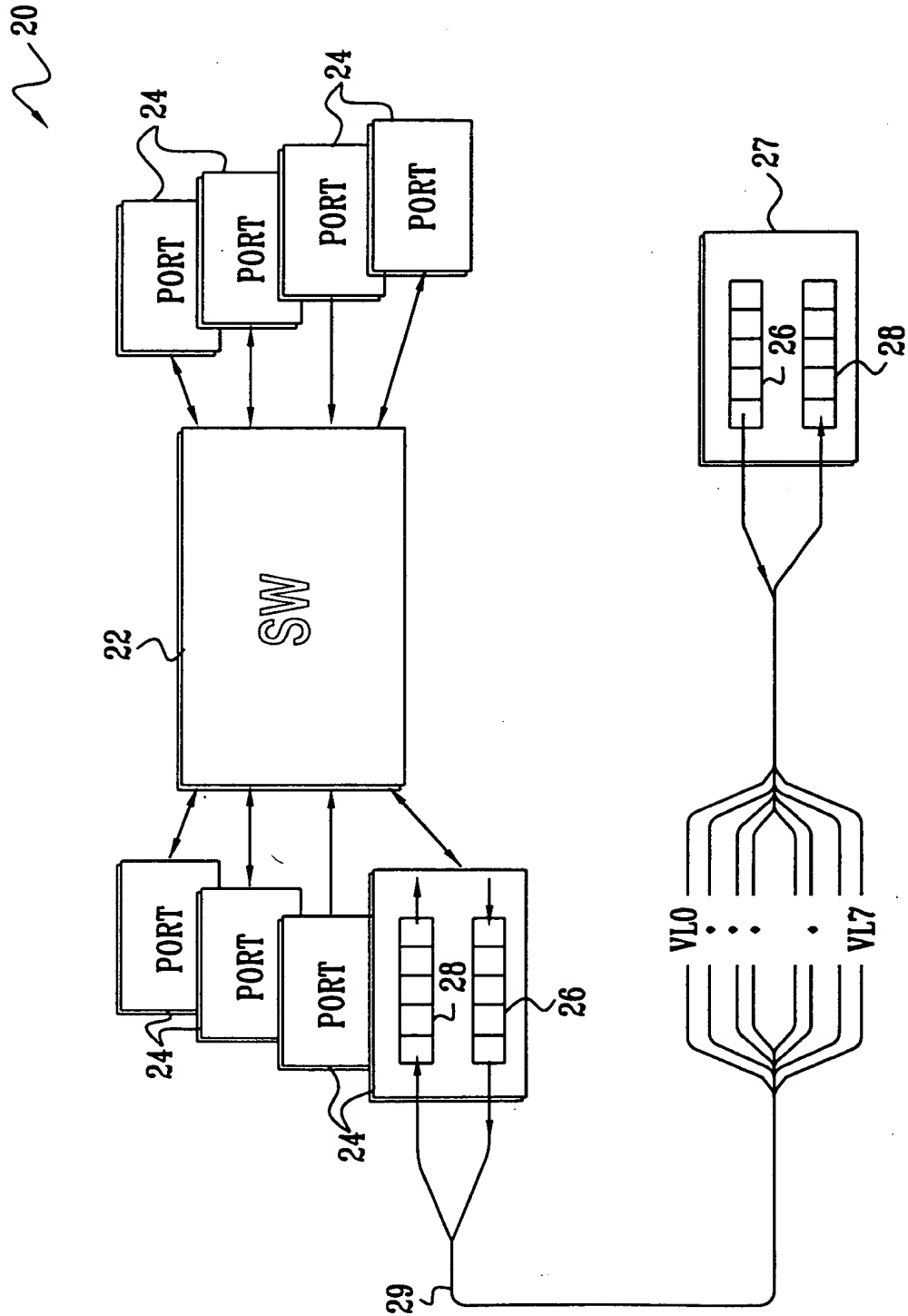


FIG. 2

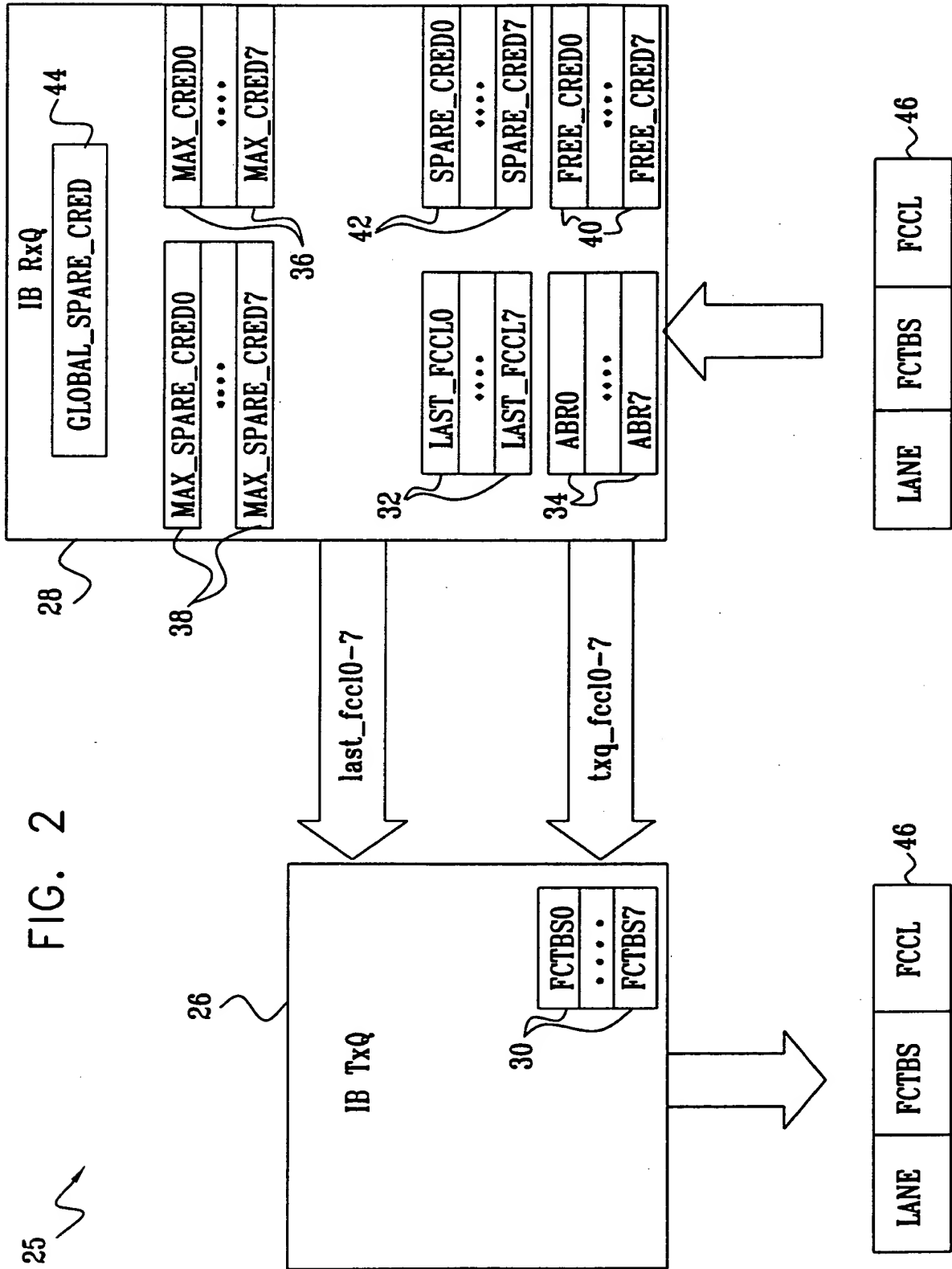


FIG. 2 is a block diagram of an IB TxQ and IB RxQ. The IB TxQ (26) includes a register set 30 with fields FCTBS0, ..., FCTBS7. The IB RxQ (28) includes a register set 36 with fields MAX_SPARE_CRED0, ..., MAX_SPARE_CRED7, a register set 32 with fields LAST_FCCL0, ..., LAST_FCCL7, a register set 42 with fields SPARE_CRED0, ..., SPARE_CRED7, a register set 40 with fields FREE_CRED0, ..., FREE_CRED7, and a register set 44 with fields MAX_CRED0, ..., MAX_CRED7. The IB RxQ (28) also includes a GLOBAL_SPARE_CRED register (44). Two large arrows indicate data flow: 'last_fcc10-7' from the IB TxQ to the IB RxQ, and 'txq_fcc10-7' from the IB RxQ to the IB TxQ. On the right, two identical register blocks (46) are shown, each containing LANE, FCTBS, and FCCL fields. A large arrow points from the IB RxQ towards these register blocks.

FIG. 3

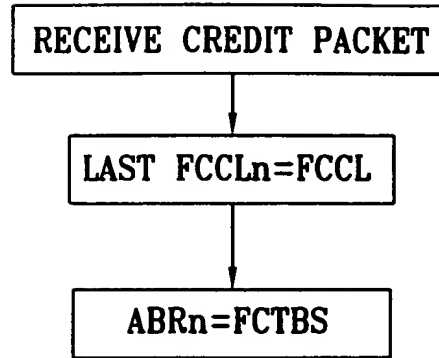


FIG. 4

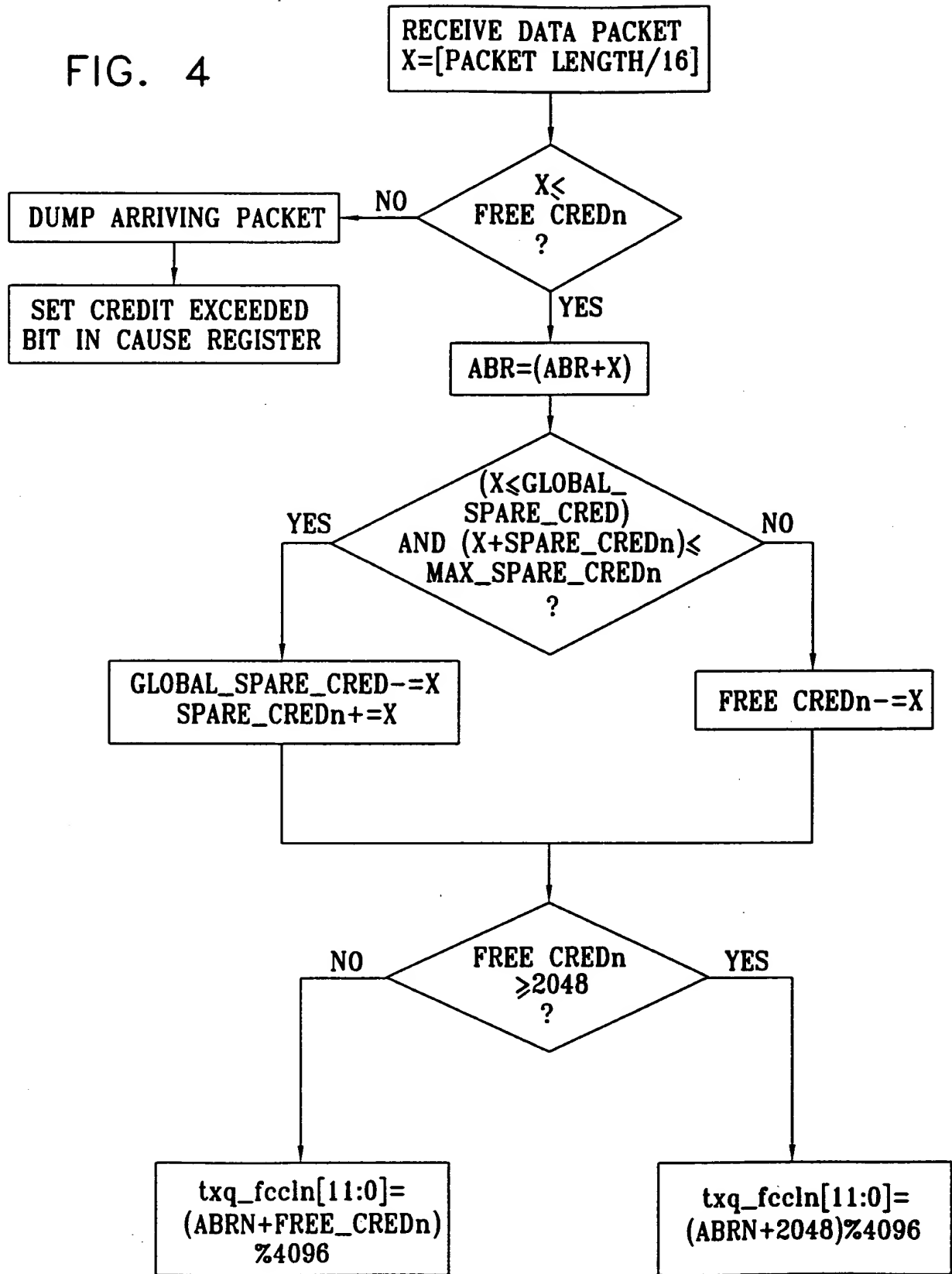


FIG. 5

